Liquid-Markets-Solutions (LMS), conceptualizes, designs & implements patented & proven industry-leading hardware-based ultra-low-latency and ultra-high-capacity connectivity, market access, risk mitigation, and bespoke solutions for use in financial services and other industries.

Curious about our technology? Take a look at some of our patents:

- Validating an Electronic Order Transmitted Over a Network…With a Hardware Device
- Distributed Network Packet Processing
- Zero-Latency Message Processing With Validity Checks
- Transaction Encoding and Verification By Way Of Data-Link Layer Fields

With locations in the United States, Japan, Singapore, and now Switzerland, LMS is emerging as a global player operating on the cutting-edge of technology to deliver the world’s fastest and highest capacity network-edge computing solutions.

LMS prides itself on operating an equal-opportunity environment free of discrimination and respectful of heritage, beliefs and lifestyles.

We’re technologists first and foremost and love to tinker (whether with FPGA chips, network stack technology, heatsinks, changing sparkplugs in a car, or crafting a mailbox from spare scraps of wood). We’re all about working together and solving problems.

To support its growth LMS is seeking hardware engineers to join its team in Zug.

Primary day-to-day duties include:

- Hands-on interaction w/advanced technology in a team-oriented & supportive environment
- Participate in business requirements discussions and:
  - propose, and/or evaluate, solutions to technological and performance requirements
  - conduct research to identify possible solutions to requirements
  - transform validated and approved solutions into high-level architecture design
- Create and/or modify design schematics and circuitry at a unit and system level
- Execute logic simulations of RTL to verify conformance, functionality, and timing reliability
- Design and implement performance benchmarking tools and plans
- Use agreed-upon tools to manage and maintain project components including code and design documentation, code base/version control, and project management
- Create and/or modify design and code implemented in a software language such as C/C++, Python, Golang, NodeJS, Java, etc
- Conduct peer code reviews

If you have experience with one or more of below, we want to meet you!

- FPGA design experience including timing closure, design cycle, simulation, and test tools
- One or more of Verilog, SystemVerilog, VHDL, or Vivado
- Simulator (ex. Modelsim) experience
- Working knowledge of Ethernet/IP/TCP and familiarity with packet processing
- Experience in PCI-Express device driver / Linux kernel module development.
- C/C++/JAVA/Embedded Software experience.
- Use of Linux (RHEL/Centos) administration and network tools.
- Working knowledge of Linux and scripting languages (Shell, Perl).
- Working knowledge of distributed version-control systems (Git preferred).

LMS is an international environment, with US, Swiss, Scottish, and French citizens among its employees, we're all bilingual in one form or another, but our common languages are English and Technology.

Don't be shy! Remember, nothing ventured is nothing gained, if some or all of above sounds interesting, please reach out!

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